

Mehran University of Engineering & Technology, Jamshoro
Department of Electronic Engineering

17ES Final Year Projects Tentative Timeline for Students

All the students of 17ES batch are hereby informed to follow the following tentative schedule regarding Final year project design. Students are advised to work on their FYPs accordingly.

FYP Activity	Duration/Date
Call for Proposals and Group Formation	Nov 9, 2020
Project Proposal Submission deadline	Nov 16, 2020
Display of List of Approved Project Proposals	Nov 30, 2020
Conduct of Initial Seminars	2 nd week of 7 th Semester
Display of Initial Seminars Results	3 rd week of 7 th Semester
Repeat Initial Seminar	Start of 5 th week of 7 th Semester
Final comments/result Display	End of 5 th week of 7 th Semester
Training/Workshop Sessions for FYP	
<input checked="" type="checkbox"/> OBE based FYP	Start of 7 th Semester
<input checked="" type="checkbox"/> Prospect Funding opportunities for FYP	7 th Semester after Mid
<input checked="" type="checkbox"/> Academic Writing Techniques/Strategies and Plagiarism policy awareness (Turnitin)	7 th Semester after Mid
<input checked="" type="checkbox"/> Drafting of thesis and Presentation	Before 7 th Semester Final Examination
<input checked="" type="checkbox"/> Research paper writing <input checked="" type="checkbox"/> Review paper writing	Start of 8 th Semester
<input checked="" type="checkbox"/> Mendeley (Referencing Tool)	8 th Semester before Mid
<input checked="" type="checkbox"/> Latex (Thesis Writing Tool)	8 th Semester after Mid
First Viva Exams	Examination Dept. will announce dates
Poster, Presentation, Project Display & Final Viva Exams	Examination Dept. will announce dates