



MEHRAN UNIVERSITY OF ENGINEERING AND TECHNOLOGY

FYP ASSESSMENT SHEET BY SUPERVISOR (7th Semester)

Department: **ELECTRONIC ENGINEERING** Batch: **16ES** Date of Conduct of Examinations: **24-03-2019**

Project Title: _____

Group No: _____

Name of Project Supervisor: _____

S. No	Roll No:	Criteria 1 Understanding & Originality			Criteria 2	Criteria 3	Criteria 4	Criteria 5	Criteria 6	Criteria 7	Total marks Obtained		
		Attendance	Sessional	Viva	Literature Review	Viva	Methodology	Design & Implementation	Progress	Presentation	Social Impact	Out of 50 Marks	
		10	15	15	10	-	--	--	--	--	Sessional	Viva	
1.	16ES												
2.	16ES												
3.	16ES												
4.	16ES												
5.	16ES												
6.	16ES												

Signature

Supervisor